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Q. 6.10: Design a serial 2's completer with a shift register and a flip-flop. The binary number **Digital Design Lec 7 part 2** **تار مراح م و ف رئاودلا و ف** Computer Logic Design M Morris Mano Part 1 Boolean Algebra || Exercise Problems || Digital Logic And Design || Part 1 || Urdu/Hindi Digital design lecture 1 **Digital Design: Q. 1.10: Convert the following binary numbers to hexadecimal and to decimal: (a), (b** Digital Design By Morris Mano

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M. Morris Mano is an Emeritus Professor of Computer Engineering at the California State University, Los Angeles. His notable works include the Mano Machine, i.e. a theoretical computer that contains a central processing unit, random access memory, and an input-output bus. M. Morris Mano has authored

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